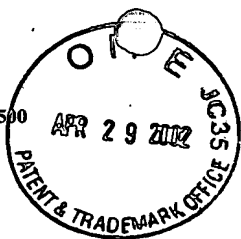


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SCHIFF HARDIN & WAITE

PATENT DEPARTMENT  
6600 SEARS TOWER  
233 SOUTH WACKER DRIVE  
CHICAGO, ILLINOIS 60606

2815

In re application of: Gerald DEBOY et al.

Serial No.: 09/530,553

GROUP ART UNIT: 2815

Filed: July 21, 2000

EXAMINER: P. B. K. II

For: HIGH VOLTAGE RESISTANT EDG STRUCTURE FOR SEMICONDUCTOR COMPONENTS

AMENDMENT "B"

Assistant Commissioner for Patents  
Washington D.C. 20231

SIR:

Transmitted herewith is an amendment in the above-identified application.

☐ No additional fee is required.

The fee has been calculated as shown below.

CLAIMS AS AMENDED						
	(2) CLAIMS REMAINING AFTER AMENDMENT		(4) HIGHEST NO. PREVIOUSLY PAID FOR	(5) PRESENT EXTRA	(6) RATE	(7) ADDITIONAL FEE
TOTAL CLAIMS	* 15	MINUS	** 20	X 0	( ) X 9.00 ( ) X 18.00	
INDEP. CLAIMS	* 1	MINUS	3	X 0	( ) X 40.00 ( ) X 80.00	
Application amended to contain any multiple dependent claims not previously paid for.				( ) YES ( ) NO	( ) \$135.00 ( ) \$270.00 ONE TIME	
				TOTAL ADDITIONAL FEE FOR THIS AMENDMENT		- 0 - \$

\* If the entry in Column 2 is less than the entry in Column 4, write "0" in Column 5.

- \*\* If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20 write "20" in this space.
- ☐ Applicants petition the Commissioner of Patents and Trademarks to extend this time for response to the Office Action dated \_\_\_\_ for \_\_\_\_ months so that the period for response is extended to \_\_\_\_\_. A check in the amount of \$ \_\_\_\_\_ is attached to cover the cost of the extension. Any deficiency or overpayment should be charged or credited to deposit account No. 501519. A duplicate copy of this sheet is enclosed.
- ☐ A check in the amount of \$ \_\_\_\_\_ is attached.
- ☐ A check for \$ \_\_\_\_\_ accompanying IDS under 37 CFR 1.97(c) is attached
- ☐ A check for \$ \_\_\_\_\_ and Petition for Consideration of IDS under 37 CFR 1.97(d) is attached.
- ☐ The Commissioner is hereby authorized to charge any additional fees which may be required, or to credit any overpayment to account No. 501519. A duplicate of this sheet is enclosed.
- When phoning re this application, please call (312) 258-5500.

SCHIFF HARDIN & WAITE (Customer Number: 26574)

Patent Department

BY

*Melvin A. Robinson* 31870

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on April 18, 2002.

Melvin A. Robinson

NAME OF APPLICANT'S ATTORNEY

SIGNATURE

April 18, 2002

DATE



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5-10-2  
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

AMENDMENT "B"

APPLICANT: Gerald DEBOY et al. GROUP ART UNIT: 2815  
SERIAL NO.: 09/530,553 EXAMINER: P. Brock II  
FILING DATE: July 21, 2000  
INVENTION: HIGH VOLTAGE RESISTANT EDGE STRUCTURE FOR  
SEMICONDUCTOR COMPONENTS

Hon. Assistant Commissioner for Patents  
Washington D.C. 20231

SIR:

In response to the Office Action dated January 18, 2002, amend the above identified application as follows:

IN THE SPECIFICATION

Amend the paragraph beginning on page 7, line 21, as follows:

B' In Figure 1, the source zones 9 and the base zones 8 are connected in known fashion to the source electrode 10, and thus to the source terminals S, via contact holes 10'. This shunting of the base zone 8 and the source zone 9 makes it possible to keep a parasitic bipolar transistor from being turned on there.

IN THE DRAWINGS

A Submittal of Proposed Drawing Change is submitted herewith to add the reference character 10' to Figure 1.

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